

Docket Number: 081498-0308307
Client Reference: GNC25US



IFW
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of

FRAMPTON E ELLIS

Group Art Unit: 2153

Application No.: 10/802,049

Examiner:

Filed: March 17, 2004

Confirmation No.: 1713

For: GLOBAL NETWORK COMPUTERS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom. Applicants respectfully request the Examiner return an initialed copy of the enclosed Form PTO-1449 to Applicants with the next Office communication to indicate that the references has been considered, per MPEP § 609.

This Information Disclosure Statement is being filed before the mailing date of the first Office Action on the merits in the present application. No certification or fee is required.

These references were cited in three Office Actions in three co-related U.S. applications.

Respectfully Submitted,

Carlo M. Cotrone
Registration Number 48715
Customer Number: 00909

Date: August 11, 2004

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

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| Sheet | 1 | of | 1 |
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Complete if Known

| | |
|------------------------|------------------|
| Application Number | 10/802,049 |
| Filing Date | 03/17/2004 |
| First Named Inventor | FRAMPTON E ELLIS |
| Art Unit | 2153 |
| Examiner Name | |
| Attorney Docket Number | 081498-0308307 |

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

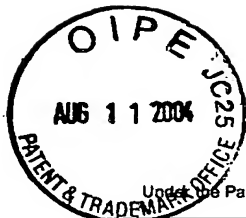
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.¹ Applicant's unique citation designation number (optional).² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3).⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/08b (08-03)
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| Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | | Complete if Known | | | |
| | | Application Number | 10/802,049 | | |
| | | Filing Date | 03/17/2004 | | |
| | | First Named Inventor | FRAMPTON E ELLIS | | |
| | | Art Unit | 2153 | | |
| | | Examiner Name | | | |
| Sheet | 1 | of | 1 | Attorney Docket Number | 081498-0308307 |

| NON PATENT LITERATURE DOCUMENTS | | | |
|---------------------------------|-----------------------|---|----------------|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | A | NONE, "PC vision: Intel unveils plans to bring PCs to vehicles", EDGE: Work Group Computing Report, EDGE Publishing, p. 1-2, (October 28, 1996). | |
| | B | LI, YAO, "Free-space Optical Bus-based WDMA Interconnects for Parallel Computation," LEOS '92 Conference Proceedings, Lasers and Electron-Optics Society Annual Meeting, p. 588-589, (November 16-19, 1992). | |
| | C | DICKINSON ET AL., "An Integrated Free Space Optical Bus," 1989 IEEE International Conference on Computer Design" VLSI In Computers and Processors, p. 62-65, (October 2-4, 1989). | |
| | D | NATARAJAN ET AL, "Bi-Directional Optical Backplane Bus for General Purpose Multi-Processor," Journal of Lightwave Technology, Vol. 13 (No. 6), p. 1031-1040, (June 6, 1995). | |
| | E | ZHAO ET AL, "General Purpose Bidirectional Optical Backplane: High Performance Bus for Multiprocessor Systems," Massively Parallel Processing Using Optical Interconnections, 2nd International Conference, p. 188-195, (October 23-24, 1995). | |
| | F | WU ET AL, "Microprocessor Control Signal Transmission through Optical Fiber," Conference Record of 1992 IEEE Industry Applications Society Annual Meeting, p. 1747-1750, (October 4-9, 1992). | |
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